

Introduction To Digital Logic Design Hayes

Brock J. LaMeres

Introduction to Digital Logic Design John Patrick Hayes,1993 Textbook

Introduction to Digital Logic Design John P. Hayes,1996-01

Introduction to Digital Logic Design John Patrick Hayes,1993-01-01

Introduction to Digital Electronics J. Crowe,Barrie Hayes-Gill,1998-04-10 Introduction and Basic Principles -- introduction * Boolean algebra * Number systems * Combinational Logic -- Introduction * Sum of products representation * The negative logic approach * Minimalisation * Common combinational circuits * The 4-bit adder; a design example * Systolic (iterative) arrays * General sums of products circuits * Hazards in combinational circuits * Fault diagnosis * Sequential circuits -- Introduction * Asynchronous sequential circuits * Flip flops * Registers * Counters * Synchronous sequential circuits * Choosing a Means of Implementation -- Introduction * Technology * The level of integration * Possible Design Routes -- Discrete implementation * PAL, PLA, EPLD implementation * Mask programmable gate arrays * Field programmable gate arrays * Standard cell * Full custom * VHDL * Digital Meets Analogue * Index.

Digital Logic Design Brian Holdsworth,Clive Woods,2002-11-01 New, updated and expanded topics in the fourth edition include: EBCDIC, Grey code, practical applications of flip-flops, linear and shaft encoders, memory elements and FPGAs. The section on fault-finding has been expanded. A new chapter is dedicated to the interface between digital components and analog voltages. *A highly accessible, comprehensive and fully up to date digital systems text *A well known and respected text now revamped for current courses *Part of the Newnes suite of texts for HND/1st year modules

Introduction to Digital Logic Design Tariq Jamil,Iftaquaruddin Mohammed,2011-09 Various books on the topic of digital logic design are available in the market today which tend to focus on presenting detailed description of the topics associated with this subject area. Unfortunately, because of limited time during the semester, most students of digital logic design are unable to read each and every page of these books to ascertain the main objective of each topic and, therefore, require a more succinct and to-the-point approach towards learning about these fundamental concepts. This book is a compilation of lectures in digital logic design resulting from teaching of this subject by the authors during the past several years and is intended to provide concise description of the topic commonly covered in a one-semester course. Typical questions or problems which may be asked in examinations from the students are indicated at the conclusion of each chapter to assist students with the formulation of their study plans. It is hoped that this book will assist both students and practicing computer engineers in understanding of the fundamental concepts of digital logic design and can be used as a reference book throughout their professional career.

Electrical and Computer Engineering Rajiv Kapadia,2015-08-13 An excellent introduction to the digital world in engineering, Introduction to Digital Logic Design explains the simple concepts behind digital logic design from logic gates all the way to the design of sequential machines. Over the course of the eight chapters of the book students explore number systems and codes, simple logic states, boolean algebra, working with logic equations, and simplifying logic functions. They also work with arithmetic in binary systems, common combinational logic functions, counters, and sequential logic. Each chapter includes practical problems that allow for immediate application of the skills and concepts. All material is based on extensive class testing. Simple yet rigorous, Introduction to Digital Logic Design helps first-semester students see the big picture in logic design and doesn't overwhelm them with extraneous details. The text is suitable for first-year engineering, computer science, and information science courses. Rajiv Kapadia earned his Ph.D. at the University of Oklahoma. Dr. Kapadia is an associate professor of electrical and computer engineering and technology at Minnesota State University, Mankato.

EBOOK: Fundamentals of Digital Logic Stephen Brown,Zvonko Vranesic,2008-07-16 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits. The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small

examples, which are easy to understand. Then, a modular approach is used to show how larger circuits are designed. VHDL is a complex language so it is introduced gradually in the book. Each VHDL feature is presented as it becomes pertinent for the circuits being discussed. While it includes a discussion of VHDL, the book provides thorough coverage of the fundamental concepts of logic circuit design, independent of the use of VHDL and CAD tools. A CD-ROM containing all of the VHDL design examples used in the book, as well as Altera's Quartus II CAD software, is included free with every text.

Fundamentals of Digital Logic and Microcomputer Design M. Rafiquzzaman, 2005-07-08
Fundamentals of Digital Logic and Microcomputer Design, has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers. In this Fifth Edition, the author focuses on computer design at three levels: the device level, the logic level, and the system level. Basic topics are covered, such as number systems and Boolean algebra, combinational and sequential logic design, as well as more advanced subjects such as assembly language programming and microprocessor-based system design. Numerous examples are provided throughout the text. Coverage includes: Digital circuits at the gate and flip-flop levels Analysis and design of combinational and sequential circuits Microcomputer organization, architecture, and programming concepts Design of computer instruction sets, CPU, memory, and I/O System design features associated with popular microprocessors from Intel and Motorola Future plans in microprocessor development An instructor's manual, available upon request Additionally, the accompanying CD-ROM, contains step-by-step procedures for installing and using Altera Quartus II software, MASM 6.11 (8086), and 68asmsim (68000), provides valuable simulation results via screen shots. Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems.

Introduction to Logic Design Svetlana N. Yanushkevich, Vlad P. Shmerko, 2008-01-25 With an abundance of insightful examples, problems, and computer experiments, Introduction to Logic Design provides a balanced, easy-to-read treatment of the fundamental theory of logic functions and applications to the design of digital devices and systems. Requiring no prior knowledge of electrical circuits or electronics, it supplies the

Introduction to Logic Circuits & Logic Design with Verilog Brock J. LaMeres, 2019 This textbook for courses in Digital Systems Design introduces students to the fundamental hardware used in modern computers. Coverage includes both the classical approach to digital system design (i.e., pen and paper) in addition to the modern hardware description language (HDL) design approach (computer-based). Using this textbook enables readers to design digital systems using the modern HDL approach, but they have a broad foundation of knowledge of the underlying hardware and theory of their designs. This book is designed to match the way the material is actually taught in the classroom. Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics. The author has designed the presentation with learning goals and assessment at its core. Each section addresses a specific learning outcome that the student should be able to do after its completion. The concept checks and exercise problems provide a rich set of assessment tools to measure student performance on each outcome.

Engineering Digital Design Richard F. Tinker, 2000-01-07 The options include the lumped path delay (LPD) model or NESTED CELL model for asynchronous FSM designs, and the use of D FLIP-FLOPs for synchronous FSM designs. The background for the use of ADAM is covered in Chapters 11, 14 and 16 of the REVISED 2nd Edition. [5] A-OPS design software: A-OPS (for Asynchronous One-hot Programmable Sequencers) is another very powerful productivity tool that permits the design of asynchronous and synchronous state machines by using a programmable sequencer kernel. This software generates a PLA or PAL output file (in Berkeley format) or the VHDL code for the automated timing-defect-free designs of the following: (a) Any 1-Hot programmable sequencer up to 10 states. (b) The 1-Hot design of multiple asynchronous or synchronous state machines driven by either PLDs or RAM. The input file is that of a state table for the desired state machine.-

Digital Electronics 1 Tertulien Ndjountche, 2016-06-17 The omnipresence of electronic devices

in our everyday lives has been accompanied by the downscaling of chip feature sizes and the ever increasing complexity of digital circuits. This book is devoted to the analysis and design of digital circuits, where the signal can assume only two possible logic levels. It deals with the basic principles and concepts of digital electronics. It addresses all aspects of combinational logic and provides a detailed understanding of logic gates that are the basic components in the implementation of circuits used to perform functions and operations of Boolean algebra. Combinational logic circuits are characterized by outputs that depend only on the actual input values. Efficient techniques to derive logic equations are proposed together with methods of analysis and synthesis of combinational logic circuits. Each chapter is well structured and is supplemented by a selection of solved exercises covering logic design practices.

Digital Electronics 2 Tertulien Ndjountche, 2016-08-16 As electronic devices become increasingly prevalent in everyday life, digital circuits are becoming even more complex and smaller in size. This book presents the basic principles of digital electronics in an accessible manner, allowing the reader to grasp the principles of combinational and sequential logic and the underlying techniques for the analysis and design of digital circuits. Providing a hands-on approach, this work introduces techniques and methods for establishing logic equations and designing and analyzing digital circuits. Each chapter is supplemented with practical examples and well-designed exercises with worked solutions. This second of three volumes focuses on sequential and arithmetic logic circuits. It covers various aspects related to the following topics: latch and flip-flop; binary counters; shift registers; arithmetic and logic circuits; digital integrated circuit technology; semiconductor memory; programmable logic circuits. Along with the two accompanying volumes, this book is an indispensable tool for students at a bachelors or masters level seeking to improve their understanding of digital electronics, and is detailed enough to serve as a reference for electronic, automation and computer engineers.

Digital Electronics 3 Tertulien Ndjountche, 2016-10-20 This third volume in the comprehensive Digital Electronics series, which explores the basic principles and concepts of digital circuits, focuses on finite state machines. These machines are characterized by a behavior that is determined by a limited and defined number of states, the holding conditions for each state, and the branching conditions from one state to another. They only allow one transition at a time and can be divided into two components: a combinational logic circuit and a sequential logic circuit. The approach is gradual and relatively independent of each other chapters. To facilitate the assimilation and practical implementation of various concepts, the book is complemented by a selection of practical exercises.

Digital Design Techniques and Exercises Vaibhav Taraate, 2021-12-09 This book describes digital design techniques with exercises. The concepts and exercises discussed are useful to design digital logic from a set of given specifications. Looking at current trends of miniaturization, the contents provide practical information on the issues in digital design and various design optimization and performance improvement techniques at logic level. The book explains how to design using digital logic elements and how to improve design performance. The book also covers data and control path design strategies, architecture design strategies, multiple clock domain design and exercises, low-power design strategies and solutions at the architecture and logic-design level. The book covers 60 exercises with solutions and will be useful to engineers during the architecture and logic design phase. The contents of this book prove useful to hardware engineers, logic design engineers, students, professionals and hobbyists looking to learn and use the digital design techniques during various phases of design.

Principles of Modern Digital Design Parag K. Lala, 2007-07-16 PRINCIPLES OF MODERN DIGITAL DESIGN FROM UNDERLYING PRINCIPLES TO IMPLEMENTATION—A THOROUGH INTRODUCTION TO DIGITAL LOGIC DESIGN With this book, readers discover the connection between logic design principles and theory and the logic design and optimization techniques used in practice. Therefore, they not only learn how to implement current design techniques, but also how these techniques were developed and why they work. With a deeper understanding of the underlying principles, readers become better problem-solvers when faced with new and difficult digital design

challenges. Principles of Modern Digital Design begins with an examination of number systems and binary code followed by the fundamental concepts of digital logic. Next, readers advance to combinational logic design. Armed with this foundation, they are then introduced to VHDL, a powerful language used to describe the function of digital circuits and systems. All the major topics needed for a thorough understanding of modern digital design are presented, including: Fundamentals of synchronous sequential circuits and synchronous sequential circuit design Combinational logic design using VHDL Counter design Sequential circuit design using VHDL Asynchronous sequential circuits VHDL-based logic design examples are provided throughout the book to illustrate both the underlying principles and practical design applications. Each chapter is followed by exercises that enable readers to put their skills into practice by solving realistic digital design problems. An accompanying website with Quartus II software enables readers to replicate the book's examples and perform the exercises. This book can be used for either a two- or one-semester course for undergraduate students in electrical and computer engineering and computer science. Its thorough explanation of theory, coupled with examples and exercises, enables both students and practitioners to master and implement modern digital design techniques with confidence.

Digital Logic Design John F. Passafiume, Michael Douglas, 1985

Fundamentals of Digital Logic with VHDL Design Stephen D. Brown, Zvonko G. Vranesic, 2005-01-01

Digital Logic Design Ming-Bo Lin, 2021-01-11 Digital Logic Design is a comprehensive textbook, which aims to provide entry-level readers a quick start to the field of digital logic design so as to facilitate them with the capability suitable for the versatility of social change and interdisciplinary learning. This textbook can be used as a textbook for classroom use in the fields of electronics, electrical, computer science, information engineering, mechanical, and soon. The salient features of this textbook are as follows: 1. Introduce incrementally the principles of digital logic design and exemplify each basic theme and concept with abundant illustrations. 2. Detail design principles of various combinational modules, including decoders, encoders, multiplexers, demultiplexers, arithmetic circuits, and so on. 3. Introduce design principles of various sequential modules, including counters, registers, shift registers, sequence generators, etc. 4. Address the structures, features, and applications of PLD/FPGA devices. 5. Exemplify applications of CPLD/FPGA devices with Verilog HDL modules. 6. Provide 20 basic and application experiments of digital logic to help readers verify the consistence of digital logic between principles and practice. 7. Include an abundance of review questions in each section to help readers evaluate their understandings about the section. 8. Deal with Verilog HDL concisely in relevant sections so as to make the reader understand how to describe a logic circuit in Verilog HDL precisely. Digital Logic Design is an ideal textbook for the digital logic design course in the fields of electronics, electrical, computer science, information engineering, mechanical, etc, or serves as a valuable reference book for self-study.

Introduction To Digital Logic Design Hayes Book Review: Unveiling the Magic of Language

In a digital era where connections and knowledge reign supreme, the enchanting power of language has become more apparent than ever. Its power to stir emotions, provoke thought, and instigate transformation is truly remarkable. This extraordinary book, aptly titled "**Introduction To Digital Logic Design Hayes**," published by a very acclaimed author, immerses readers in a captivating exploration of the significance of language and its profound affect our existence. Throughout this critique, we will delve in to the book is central themes, evaluate its unique writing style, and assess its overall influence on its readership.

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